



Digital Design and Verification Engineer

Description

Join the semify team and contribute to our pioneering digital design services, supporting innovative technologies for a diverse array of clients. We are a dynamic company based in Graz, Austria, working on exciting and cutting-edge projects, focusing on mixed-signal ASICs. We are seeking experienced Digital Design and Verification Engineers to join our versatile team.

Key Responsibilities

- Implement and verify digital designs, ensuring they meet the highest standards of quality and performance.
- Develop FPGA-based prototypes to validate and demonstrate design concepts.
- Directly interact with industry-leading clients to understand their challenges and provide effective solutions.
- Coordinate teams of engineers, both onsite and remote, fostering collaboration and innovation.

Requirements

- Minimum of 3 years of experience in digital design of ASICs and/or FPGAs.
- Strong knowledge of at least one Hardware Description Language (HDL), with SystemVerilog preferred.
- Ability to implement synchronous digital designs for ASICs or FPGAs.
- Experience in verifying digital designs (UVM preferred).
- Proficiency in writing and understanding Python scripts.
- Experience with version control systems (Git preferred).
- Enjoy working as part of a distributed team.

Benefits

- **Professional Development:** Engage in a diverse array of projects and benefit from our robust knowledge-sharing infrastructure. We utilize cutting-edge tools like Cocotb to ensure you remain at the forefront of industry advancements.
- **Relocation Support:** We provide comprehensive assistance with relocation and work permit processes, ensuring a smooth transition for international talent joining our team.
- **Skills Analysis:** Our unique skills analysis program identifies and maps your competencies, supporting your long-term professional growth.
- **Flexible Work Environment:** We offer a flexible work environment that empowers our team members to balance personal and professional commitments effectively.

Payment

This position is endowed with a yearly salary of € 50.000-65.000 based on 38.5 hours (over payment possible). The collective agreement for "Informationstechnologie" is applied. Semify offers also a benefits package that includes meal vouchers and subsidies for public transportation.

Contact

We encourage you to include a cover letter with your application, giving us insight into your current situation and future aspirations. We look forward to connecting with you and helping you start your career journey with semify.

Don't miss out on this unique opportunity to work on cutting-edge projects and to develop your skills as a Digital Design and Verification Engineer. Apply now at office@semify-eda.com and become a part of the semify team!

